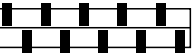


AD - DMA



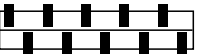
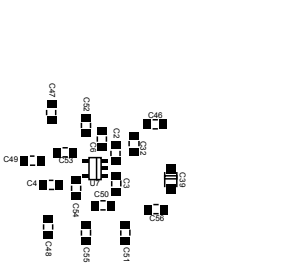
PORT E



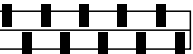
PORT B



J3



PORT C/D



PORT F



PORT G



J2



J4